



Advt. No. CDAC/BLR/ACR/02/2025

Centre for Development of Advanced Computing (C-DAC), is a Scientific Society of the Ministry of Electronics and Information Technology, Government of India. C-DAC has today emerged as a premier R&D organization in ICT&E (Information, Communications Technologies and Electronics) in the country, working on strengthening national technological capabilities in the context of global developments in the field and responding to change in the market need in selected foundation areas. C-DAC represents a unique facet working in close junction with MeitY to implement nation's policy and pragmatic interventions and initiatives in Information Technology. As an institution for high-end Research and Development (R&D), C-DAC has been at the forefront of the Information, Communications Technologies and Electronics (ICT&E) revolution, constantly building capacities in emerging/enabling technologies and innovating and leveraging its expertise, caliber and skill sets to develop and deploy products and solutions for different sectors of the economy.

Recruitment Drive for Projects "Advanced Computing Research (ACR)" and Quantum Computing (QT)

In line with the Government of India's vision of "Viksit Bharat" by 2047 and make India self-reliant in HPC/AI technologies, C-DAC has launched project "**Advanced Computing Research (ACR)**" in collaboration with a leading industry partner.

Project ACR aims at developing indigenous processors based on RISC-V ISA (CPU, GPGPU, AI/ML accelerator), scale-up and scale-out networks, and design world class HPC/AI systems with integrated photonics and robust software ecosystem.



^ TOP

C-DAC, Bengaluru Invites online applications for various contractual positions at all levels for Centres/locations across India.

The candidates should possess relevant skill sets in any of the following domain areas:

C-DAC is looking for bright engineers and professionals, both fresher and experienced, to achieve this objective. The relevant skill sets, domains, sub domains and qualifications are described below:

Domain Areas

Sr No	Domain Areas	No of Posts	Sub-domains
1	Chip Design	180	i. Core Digital Design ii. Verification iii. Physical Design
2	Electronics Hardware Design	25	i. Circuit Design ii. Simulation and Analysis iii. Hardware Testing and Debugging iv. Server Design v. Tools & Software vi. Knowledge of Standards and Compliance
3	Software Ecosystem Development	65	i. Programming skills and Computer fundamentals ii. Compiler and Runtime iii. Device Drivers iv. Profiling and Debugging v. Development Tools & Middleware vi. AI/ML/DL



Sr No	Domain Areas	No of Posts	Sub-domains
4	Photonics	10	i. Proficiency in photonic device EDA tools, fabrication, packaging, and characterization ii. Design, analyse and optimize photonic devices

General Terms and Conditions

1. Terms of Engagement:

- The positions / requirements are purely on contract basis on consolidated emoluments, initially for a period **upto 3 years** or completion of the project, whichever is earlier. The selected candidates may be assigned to another project based on organizational requirement.
- Contract may be considered for extension for a further period based on performance of the employee & requirement and duration of project.
- The candidates engaged under this shall be on contract and shall not derive any right or claim to have a regular post/ appointment in C-DAC.
- C-DAC is involved in projects at various location across the India, hence a selected candidate may be deputed or transferred from one project to another as per organizational requirement at any project location across India.

2. Qualification:

- For all the posts indicated, the candidates should have qualified from AICTE/UGC approved college/Institute or recognized University.
- In case any University/Institution or college is following an evaluation system of CGPA/DGPA/OGPA or letter grade, wherever applicable, it is mandatory for the candidate to submit proof of Percentage (%) issued by university/Institution/College and awarded class along with the degree certificate.
- Candidates who have completed all final semester examinations and are awaiting results, or who will obtain their degree by 31st July 2025, are eligible to apply.



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3. Experience:

- a. Candidate possessing relevant post qualification experience, if sought for, only needs to apply.
- b. Academy/Teaching/Research work, experience in non-profit organization, internship/project work placements are part of academic curriculum and will not be considered as experience, unless specified.
- c. Candidate should possess experience certificates/documents issued by the previous and present employer clearly indicating period of employment, salary drawn and post held.
- d. The decision of C-DAC with respect to experience will be final. Work experience indicated without supporting documents will not be considered and is liable to be rejected/cancelled without any prior intimation.
- e. The pay bracket provided against each position is for the starting pay for the min. experience required for the said post. Candidates with higher experience commensurate with the experience bracket given will be offered additional increments as per C-DAC Policy.

4. Age limit and Relaxation:

- a. Upper Age limit will be as specified against each post
- b. Applicants belonging to reserved category (SC/ST/OBC) / physically challenged/Ex-servicemen would be eligible for relaxations according to the 'Government of India' norms.
- c. The cut-off date for ascertaining the age and experience will be Last Date of submission of application.
- d. Candidates seeking age relaxation need to submit a copy of caste certificate when called for document verification
- e. SSLC/SSC/ISC mark sheet/ certificate and any other valid document will be considered as proof of date of birth.
- f. The Economically Weaker Section's Certificate should be strictly in the format prescribed by DoPT. The format is available on the website www.persmin.nic.in
- g. The application of the candidates not meeting the prescribed age criteria will be reject/cancelled without any prior intimation.
- h. Candidates belonging to OBC category (Non creamy layer) should produce the certificate issued in prescribed format.

5. Selection procedure:

^ TOP

- a. Candidate should meet the eligibility criteria as specified in the advertisement.
- b. The qualification and experience prescribed, are the minimum requirements and possession of the same does not automatically make the candidates entitled to be called for written test and / or selection processes.
- c. There will be an initial screening based on the academic records and other parameters declared in the on-line application and only screened-in candidates will be considered for further selection process.
- d. The management reserves the right to increase/alter the minimum eligibility criteria/cut off limits, in the event of the number of applicants are more, for any post(s), at its discretion. Candidates will be selected on the basis of their academic credentials, experience profile, written test marks (if any), performance in the interview and such other selection processes/parameters, as adopted and deemed fit by management.
- e. Mere application and fulfillment of the mentioned qualifications etc., does not entitle a candidate to be called for written/skill test/ interview. C-DAC may restrict the number of candidates to be called to participate in interview/selection process to a reasonable limit, on the basis of qualifications and experience higher than that of the minimum prescribed in the advertisement. The candidates should, therefore, furnish all the qualifications and experience possessed in the relevant field, over and above the minimum qualifications prescribed.
- f. The selection process shall be carried out by the respective C-DAC Centre based on its requirements and only shortlisted candidates will be contacted for further process.
- g. All information submitted in the application will be verified with the original documents at the time of document verification or at any stage of the recruitment process. If any If any documents/information provided by the candidate is found to be false or incorrect or Non-conformity with the eligibility criteria, then his/her candidature is liable to be rejected/cancelled at any stage of the recruitment and selection process without any prior intimation.
- h. The candidature is also liable for rejection at any stage if C-DAC comes across any evidence/knowledge that the qualification, experience and any other particulars indicated in the application/other forms/formats are not recognized/false misleading and/or amounts to suppression of information/particulars which should have been brought to the notice of C-DAC.

6. Application fees:



^ TOP

a. No application fee is charged by C-DAC for applying to this advertisement.

7. How to apply:

- a. Before filling the online application form, Candidates should read General Terms and Conditions carefully.
- b. Candidate should read all the eligibility parameters and ensure that he/she is eligible for the post before starting to apply online.
- c. Candidate should have a **valid email id and mobile no.** which should remain valid & active till the completion of selection process.
- d. While applying online Candidates can click on the **Apply** button provided against each position for which he/she wish to apply.
- e. The candidate needs to log in with mobile no. and then fill in the OTP received on the said mobile no.
- f. On filling the correct OTP, the applicant will be directed to fill in the application form. In case you have filled in an application in our previous advertisement, the applicant will receive a prefilled editable application.
- g. Candidates shall check the prefilled application and submit it, after adding additional details by clicking on '**Submit**' button.
- h. **Candidates should scan their photograph in .jpg format (not more than 400 KB) and keep it ready before starting to apply online for uploading.**
- i. **Candidate should upload their resume in PDF format (Not more than 500 KB)**
- j. A unique application number will be generated by the system, please note this application number for future reference and use. Candidates can take a print of the application form and keep it with them for their own records.
- k. No hard copy/printed applications should be sent to C-DAC. Incomplete and defectively filled online forms shall be rejected straightway and no subsequent correspondences will be entertained in this regard.
- l. Candidates working in Government/PSUs/Govt. Autonomous bodies should also apply online in advance and print of the application form, duly filled and signed should be forwarded through proper channel to HRD Section, C-DAC, Bengaluru.



^ TOP

- m. Those who are not forwarding their application through proper channel are required to produce 'No objection certificate (NOC)' from their current employer at the time of interview, if called for, failing which they will not be permitted to attend the interview.
- n. Any dispute with regard to selection/recruitment process will be subject to Courts/Tribunals having jurisdiction over Bengaluru, Karnataka only.

8. Important Notes:

- a. C-DAC reserves the right to increase or decrease the number of posts or not to recruit against any post notified in this notification at its discretion without any notice/notification.
- b. It is the responsibility of the candidates to assess his/her own eligibility for the post for which he/she is applying in accordance with the advertisement. In case, it is found at any point of time in future during process of selection or even after appointment that candidate was not eligible as per prescribed qualification, experience etc, which could not be found at the time of selection due to whatever circumstances, his/her candidature/appointment shall be liable to be cancelled/terminated as case may be without giving any notice.
- c. Candidates those who are applying for multiple posts should submit separate application for each post.
- d. In case of ambiguity/dispute arises on account of interpretation in version other than English, English version will prevail.
- e. Candidates are not required to send printout of application or any other documents in hard copy to C-DAC.
- f. **Interview Call Letters, other correspondences** (if any) etc. will be sent to candidates only **through email** to the email id provided in their online application. No hard copy will be sent.
- g. **Mere issue of Interview call letter to candidate will not imply acceptance of candidature or selection for the post.**
- h. In case of internal candidates, please note that the finally selected candidates will have to resign from the current services and join for selected post as fresh employee.
- i. All queries pertaining to recruitment including selection process should be addressed to our Recruitment Team only through **hr-acr@cdac.in**
- j. Kindly note that we have not authorized any agent/ agency for representing C-DAC for anything related to recruitment or its processes.



- k. **C-DAC reserves the right to cancel or introduce any Personal Interview/Other selection process.** C-DAC also reserves the right to cancel/restrict/curtail/enlarge the recruitment process and/or the selection process without any notice and without assigning any reasons.
- l. **C-DAC reserves the right to fill/not fill up the post or even to cancel the whole or part of the process of recruitment without assigning any reason thereof.**
- m. All the posts will be filled as per the as applicable Rules / guidelines of C-DAC.
- n. The candidates are advised to visit C-DAC website regularly. Notice/ information, Corrigendum, Extension etc., if any, shall be published on our website www.cdac.in only.
- o. No TA/DA shall be paid for attending written examination/ interview.
- p. **Canvassing in any form will be a disqualification for selection.**

9. Important Dates:

A	Commencement of on-line Registration of application by candidates	5 th July 2025
B	Last date for on-line registration of application by candidates	31 st August 2025
C	Written Test/Interview	Will be communicated later by email only

10. Position Details

A: ACR Manpower Requirements

Sr No	Position	Experience	No of Posts	Upper Age Limit	Educational Qualification	Initial CTC with min experience
1	Design Engineer E1	Up to 3 years	203	30 Years	B E / B Tech - Electronics, Computer Science,	Initial CTC upto Rs.



^ TOP

Sr No	Position	Experience	No of Posts	Upper Age Limit	Educational Qualification	Initial CTC with min experience
					Information Technology, Information Science with 60% or equivalent CGPA PG Diploma VLSI, HPC, Embedded Systems, Artificial Intelligence (AI)/Machine Learning (ML) MCA with B Sc / BCA with 60% or equivalent CGPA M Sc Computer Science, Electronics, Mathematics with 60% or equivalent CGPA M.E / M Tech Microelectronics/VLSI, Electronic System Design, Computer Science, Artificial Intelligence / Machine learning, Applied Mathematics, Photonics Ph D Microelectronics / VLSI, Computer Science , Artificial Intelligence (AI) /Machine Learning (ML), Photonics	18.00 LPA
2	Senior Design Engineer E2	Min 3 years upto 6 years	67	33 Years	B E / B Tech - Electronics, Computer Science, IT, IS with 60% or equivalent CGPA	Initial CTC upto Rs. 21.00 LPA


[^ TOP](#)

Sr No	Position	Experience	No of Posts	Upper Age Limit	Educational Qualification	Initial CTC with min experience
					PG Diploma VLSI, HPC, Embedded Systems, AI/ML MCA with B Sc / BCA with 60% or equivalent CGPA M Sc Computer Science, Electronics, Mathematics with 60% or equivalent CGPA M.E / M Tech Microelectronics/VLSI, Electronic System Design, Computer Science, AI / ML, Applied Mathematics, Photonics Ph D Microelectronics / VLSI, Computer Science , Artificial Intelligence (AI)/Machine Learning (ML), Photonics	
3	Principal Design Engineer E3	Min 6 years upto 9 years	5	37 Years	B E / B Tech Electronics with 60% or equivalent CGPA PG Diploma VLSI M Sc Electronics, Mathematics with 60% or equivalent CGPA M.E / M Tech Microelectronics/VLSI, Electronic System Design,	Initial CTC upto Rs. 24.00 LPA


[^ TOP](#)

Sr No	Position	Experience	No of Posts	Upper Age Limit	Educational Qualification	Initial CTC with min experience
					Applied Mathematics Ph D Microelectronics / VLSI	
4	Technical Manager E4	Min 9 years upto 13 years	3	41 Years	B E / B Tech Electronics with 60% or equivalent CGPA PG Diploma VLSI M Sc Electronics, Mathematics with 60% or equivalent CGPA M.E / M Tech Microelectronics/VLSI, Electronic System Design, Applied Mathematics Ph D Microelectronics / VLSI	Initial CTC upto Rs. 36.00 LPA
5	Sr. Technical Manager E5	Min 13 years upto 18 years	1	46 Years	B E / B Tech Electronics with 60% or equivalent CGPA PG Diploma VLSI M Sc Electronics, Mathematics with 60% or equivalent CGPA M.E / M Tech Microelectronics/VLSI, Electronic System Design, Applied Mathematics Ph D Microelectronics / VLSI	Initial CTC upto Rs. 39.00 LPA


[^ TOP](#)

Sr No	Position	Experience	No of Posts	Upper Age Limit	Educational Qualification	Initial CTC with min experience
6	Chief Technical Manager E6/ Consultant	Min 18 years	1	50-65 years	B E / B Tech Electronics with 60% or equivalent CGPA PG Diploma VLSI M Sc Electronics, Mathematics with 60% or equivalent CGPA M.E / M Tech Microelectronics/VLSI, Electronic System Design, Applied Mathematics Ph D Microelectronics / VLSI	Up to Rs.42.00 LPA

B: Quantum Technologies Manpower Requirements

Sr No	Position	Experience	No of Posts	Upper Age Limit	Educational Qualification	Initial CTC with min experience
1	Design Engineer E1(Quantum Computing Algorithms & Applications)	0-3 Years	2	30 Years	B E / B Tech or M.S or M Tech or Ph.D in Physics, Quantum Technology, Computer Science or a related field with a specialization in quantum technology areas.	Initial CTC upto Rs. 18.00 LPA



^ TOP

Sr No	Position	Experience	No of Posts	Upper Age Limit	Educational Qualification	Initial CTC with min experience
2	Design Engineer E1(Quantum Photonics)	0-3 Years	2	30 Years	B E / B Tech or M.S.or M.Tech or Ph.D in Electrical Engineering, Applied Physics, Optics, Photonics, or a related field.	Initial CTC upto Rs. 18.00 LPA
3	Design Engineer E1(Control Electronics for Quantum Computing System)	0-3 Years	2	30 Years	B E / B Tech or M.S or M.Tech or Ph.D. in Electrical Engineering, Electronics Engineering, or a related field with a strong emphasis on high-speed electronics, RF/microwave design, or digital signal processing	Initial CTC upto Rs. 18.00 LPA
4	Design Engineer E1(Control Electronics for Quantum Communication System)	0-3 Years	2	30 Years	B E / B Tech, M.S.or M.Tech or Ph.D in Electrical Engineering, Electronics Engineering, or a related field with a strong emphasis on high-speed electronics, RF/microwave design, or digital signal processing.	Initial CTC upto Rs. 18.00 LPA


[^ TOP](#)

Sr No	Position	Experience	No of Posts	Upper Age Limit	Educational Qualification	Initial CTC with min experience
5	Senior Design Engineer E2 (Control Electronics for Quantum Computing System)	Min 3 years upto 6 years	2	33 Years	Ph.D. or M.S.or M.Tech. in Electrical Engineering, Electronics Engineering, or a related field with a strong emphasis on high-speed electronics, RF/microwave design, or digital signal processing.	Initial CTC upto Rs. 21.00 LPA
6	Senior Design Engineer E2(Quantum Computing Algorithms & Applications)	Min 3 years upto 6 years	2	33 Years	Master's/Ph.D in Physics, Quantum Technology, Computer Science or a related field with a specialization in quantum technology areas	Initial CTC upto Rs. 21.00 LPA
7	Senior Design Engineer E2 (Quantum Photonics)	Min 3 years upto 6 years	1	33 Years	Ph.D. or M.S.or M.Tech. in Electrical Engineering, Applied Physics, Optics, Photonics, or a related field	Initial CTC upto Rs. 21.00 LPA
8	Senior System Engineer E2(Control Electronics for Quantum	Min 3 years upto 6 years	2	33 Years	Ph.D. or M.S.or M.Tech. in Electrical Engineering, Electronics Engineering, or a related field with a strong emphasis on high-speed electronics, RF/microwave	Initial CTC upto Rs. 21.00 LPA


[^ TOP](#)

Sr No	Position	Experience	No of Posts	Upper Age Limit	Educational Qualification	Initial CTC with min experience
	Communication System)				design, or digital signal processing.	
9	Principal Design Engineer E3 (Superconducting Qubits)	Min 6 years upto 9 years	1	37 years	Ph.D. or M.S.or M.Tech in Physics, Electrical Engineering, Applied Physics, or a closely related field.	Initial CTC upto Rs. 24.00 LPA
10	Principal Design Engineer E 3 (Control Electronics for Quantum Computing System)	Min 6 years upto 9 years	1	37 Years	Ph.D. or M.S.or M.Tech. in Electrical Engineering, Electronics Engineering, or a related field with a strong emphasis on high-speed electronics, RF/microwave design, or digital signal processing.	Initial CTC upto Rs. 24.00 LPA
11	Principal Design Engineer E3 (Quantum Computing Algorithms & Applications)	Min 6 years upto 9 years	1	37 Years	Master's/Ph.D in Physics, Quantum Technology, Computer Science or a related field with a specialization in quantum technology areas.	Initial CTC upto Rs. 24.00 LPA

